P1685

Submitter Email: erwin.de.kock@nxp.com
Type of Project: Revision to IEEE Standard 1685-2014
Project Request Type: Initiation / Revision
PAR Request Date: 03 Jan 2021
PAR Approval Date: 25 Mar 2021
PAR Expiration Date: 31 Dec 2025
PAR Status: Active
Root Project: 1685-2014

1.1 Project Number: P1685
1.2 Type of Document: Standard
1.3 Life Cycle: Full Use

2.1 Project Title: Standard for IP-XACT, Standard Structure for Packaging, Integrating, and Reusing IP within Tool Flows
    Change to Title: ~~IEEE~~ Standard for IP-XACT, Standard Structure for Packaging, Integrating, and Reusing IP within Tool Flows

3.1 Working Group: C/DA/IPXACT IP-XACT Standardization Working Group(C/DA/IP-XACT)
    3.1.1 Contact Information for Working Group Chair:
    Name: Erwin de Kock
    Email Address: erwin.de.kock@nxp.com
    3.1.2 Contact Information for Working Group Vice Chair:
    None
3.2 Society and Committee: IEEE Computer Society/Design Automation(C/DA)
    3.2.1 Contact Information for Standards Committee Chair:
    Name: Dennis Brophy
    Email Address: dennis_brophy@mentor.com
    3.2.2 Contact Information for Standards Committee Vice Chair:
    None
    3.2.3 Contact Information for Standards Representative:
    None

4.1 Type of Ballot: Entity
4.2 Expected Date of submission of draft to the IEEE SA for Initial Standards Committee Ballot: Dec 2022
4.3 Projected Completion Date for Submittal to RevCom: Mar 2023

5.1 Approximate number of entities expected to be actively involved in the development of this project: 12
5.2 Scope of proposed standard: This standard describes an eXtensible Markup Language (XML) schema for meta-data documenting intellectual property (IP) used in the development, implementation, and verification of electronic systems. This schema provides both a standard method to document IP that is compatible with automated integration techniques and a standard method (generators) for linking tools into a system development framework, enabling a more flexible, optimized development environment. Tools compliant with this standard are able to interpret, configure, integrate, and manipulate IP blocks that comply with the IP meta-data description. The standard is independent of any specific design processes. It does not cover behavioral characteristics of the IP that are not relevant to integration.
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5.3 Is the completion of this standard contingent upon the completion of another standard? No
5.4 Purpose: This standard enables the creation and exchange of IP in a highly automated design

environment.

5.5 Need for the Project: As designs get larger and more complex, the electronics industry is using more IP blocks in those designs. The lack of a standard description of those blocks makes their use difficult, error-prone, and costly to implement. This revision will further address these issues and will update IEEE Std 1685-2014 based on learning from the use of the standard.

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5.6 Stakeholders for the Standard: Stakeholders for this standard include Electronic Design Automation (EDA) vendors, IP vendors, electronic systems builders and IC manufacturers.

6.1 Intellectual Property

    6.1.1 Is the Standards Committee aware of any copyright permissions needed for this project?
    Yes
    Explanation: The standard will be based on material developed by The Accellera Systems Initiative. They have agreed to transfer the copyright of this material to the IEEE for use in this proposed revision.
    6.1.2 Is the Standards Committee aware of possible registration activity related to this project?
    No

7.1 Are there other standards or projects with a similar scope? No
7.2 Is it the intent to develop this document jointly with another organization? No

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8.1 Additional Explanatory Notes: 5.2, 5.5: This revision will update IEEE Std 1685-2014 based on learning from the use of the standard. This revision will also replace non-inclusive terms.