IEEE P1685 Working Group

Meeting Agenda  
9 December 2021 / 10:00 AM – 11:30 AM (EST)  
Teleconference (Teams Video Conference ID: 126 333 222)

1. Call to Order
2. Roll Call and Disclosure of Affiliation

*Affiliation FAQs:* [*http://standards.ieee.org/faqs/affiliation.html*](http://standards.ieee.org/faqs/affiliation.html)

1. Approval of Agenda
2. Approval of Previous Meeting Minutes
   * [2021-10-28-minutes.docx](https://ieee-sa.imeetcentral.com/p1685/folder/WzIwLDEzNzk3NjAyXQ/WzIsODE0NjkzODhd/)
3. IEEE SA Patent & Copyright Policies  
   Call for Patents <https://development.standards.ieee.org/myproject/Public/mytools/mob/slideset.pdf>

Copyright <https://standards.ieee.org/ipr/copyright-materials.html>

1. Unfinished Business/Action Item Review
   * [Tracking of attendance and eligible voters](https://ieee-sa.imeetcentral.com/p1685/folder/WzIwLDEzNzEwMzA1XQ/WzIsNzQ1OTA1NjNd/)
   * Result of the formation of the Balloting Group
   * [MEC review comments](https://ieee-sa.imeetcentral.com/p1685/folder/WzIwLDEzNzk3Nzk1XQ/WzIsODE0MzY3Mzld/)
   * Two meetings held in sub groups on Nov. 16 & 18 to discuss SystemVerilog comments
   * Two meetings held in sub groups on Nov. 15 & 17 to discuss memory map / mode comments
   * [Comment 50](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODEwMzkyNTZd/w-Comment50DefaultValueForPort) Default value for port
2. Technical Presentation(s), Contribution(s) or Discussion(s)
   * Recommendation from SystemVerilog sub group  
     1. To add a Boolean attribute named phantom to the interface element.   
     2. To make the subPorts element and the structPortTypeDefs element optional.  
       
     [Comment 39](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODEwMjU1OThd/w-Comment39PhysicalPortRestrictions) Physical port restrictions

[Comment 40](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODEwMjYzNDVd/w-Comment40HierachicalSVInterfaceAndIsIO) Hierachical SV interface and isIO

[Comment 45](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODEwMzE0ODZd/w-Comment45VerilogToSystemVerilogConnection) Verilog To SystemVerilog connection

[Comment 46](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODEwMzE3Njld/w-Comment46IsIOPortReferenceIgnoreModport) isIO port reference ignore modport

[Comment 47](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODEwMzU0ODFd/w-Comment47ConsecutiveBitsSelectionAndArrayOfSvInterface) Consecutive bits selection and Array of SvInterface

* + Discussion in memory map / mode sub group  
    The WG needs to decide / clarify how modes and memory maps relate because different members have different interpretations.  
      
    **Interpretation 1.** A register transaction can “see” different modes in the path towards the registers, e.g., modeA in the address block and modeB in the register. Implications of that are:
    - Existing SCRs need to be removed.
    - A field access value needs to be computed by combining the access values of the elements on the path towards that field (addressBlock 🡪 registerFiles 🡪 register 🡪 field).
    - To define unique paths for a set of active modes, the modeRef priority values need to be unique with the scope of accessPolicies, fieldAccessPolicies, and accessRestrictions elements. In the current schema, the scope is accessPolicy, fieldAccessPolicy, and accessRestriction.

**Interpretation 2.** A register transaction can “see” only 1 mode in the path towards the registers (addressBlock 🡪 registerFiles 🡪 register 🡪 field). Note that the “single” mode concept does not apply to memoryRemap, alternateRegister, and remapAddress modes. Implications of that are:

* + - Existing SCRs need to be kept.
    - A field access value is described by the fieldAccessPolicy/access element value.
    - To define unique paths for a set of active modes, the modeRef priority values need to be unique within the scope of addressBlock/accessPolicies. In the current schema, the scope is addressBlock/accessPolicies/accessPolicy. The modeRef priority attribute in registerFile, register, field, and accessRestriction can be removed or it should be made clear what they mean. For now, it is assumed that they are removed.

[Comment 56](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODEwNDQ1OTJd/w-Comment5661312MemoryRemapmodeRef) 6.13.1.2 memoryRemap.modeRef

[Comment 57](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODEwNDQ2MTJd/w-Comment5761442cAlternateRegistermodeRefIsUnclear) 6.14.4.2.c alternateRegister.modeRef is unclear

[Comment 59](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODEwNDQ2MzVd/w-Comment59C22AccessPolicies) C.2.2 accessPolicies

[Comment 82](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODE0NTA3MjJd/w-Comment82ModeInRemapAddressIsNotConsistentWithGlobalModeManagement) mode in remapAddress is not consistent with global mode management

[Comment 84](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODE0ODI1Mzhd/w-Comment84APriorityAttributeCouldBeAddedToFieldMapModeRef) a priority attribute could be added to fieldMap modeRef

* + Discuss other [WG comments](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsNzc3NzA5MDFd/w-ReviewCommentsOnAccelleraIPXACTWGContribution) on [draft](https://ieee-sa.imeetcentral.com/p1685/folder/WzIwLDEzNzk3Nzk1XQ/WzIsODEwMzM4NTdd/) (PDF) submitted for MEC review

[Comment 61](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODEwNDQ4MTVd/w-Comment61NoUnitAttributesInSingleShotDriver) No unit attributes in singleShotDriver.

[Comment 62](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODEwNTY1MTFd/w-Comment62NamespaceInAttributeResolve) Namespace in attribute resolve

[Comment 64](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODEzNzQ2Njdd/w-Comment64AFieldWithFieldDefinitionRefCannotBeAnAlias) A field with fieldDefinitionRef cannot be an alias

[Comment 65](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODEzNzQ4Njld/w-Comment65NetlistingRules) Netlisting rules

[Comment 67](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODE0MTk1MDZd/w-Comment67UpdateTGIToSupportDefaultValues) Update TGI to support default values

[Comment 86](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODE0ODI3MDZd/w-Comment86SCR724And725NeedExamples) SCR 7.24 and 7.25 need examples

[Comment 89](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODE0OTI1NTNd/w-Comment89TypeIdentifierIsUselessInTypeDefinitions) typeIdentifier is useless in type definitions

[Comment 92](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODE1MDAyNDBd/?&pgref=) Update addressing equation (13)

[Comment 93](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODE1MDUyNzFd/w-Comment93NewSCRToCheckConsistencyOfAccessHandleSlicesSize) New SCR to check consistency of accessHandle slices size

[Comment 94](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODE1MDE3NzVd) SCR 7.13 clarification (typeIdentifier registerDefinitionGroup/alternateDefinitionGroup)

[Comment 98](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODE1MzE5MTFd/w-Comment98ReadResponseHasAmbiguousDescriptions) readResponse has ambiguous descriptions

[Comment 99](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODE1NDI5NjVd/w-Comment99StructPortTypeDefIncorrectReference) StructPortTypeDef: incorrect reference

[Comment 100](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODE1NDMwMDRd/w-Comment100TypoMissingPortDriverInElementsForViewRefC28) Typo: Missing Port Driver in elements for viewRef C.28

[Comment 101](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODE1NDMzMDVd/w-Comment101UselessDescriptionForSubPort) Useless description for subPort

[Comment 102](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODE1NDM1NTRd/w-Comment102WhyLastSubportInPortReferenceIsAWire) Why last subport in port reference is a wire ?

[Comment 103](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODE1NDY3MjZd/w-Comment103StructuredPortReference) Structured port reference

[Comment 104](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODE1NTIzMDhd/w-Comment104AlternateRegisterInAccessPolicyDescrip) alternateRegister in accessPolicy description

[Comment 105](https://ieee-sa.imeetcentral.com/p1685/doc/WzIsODE1NjI1NThd/w-Comment105CleanupTGICallsAccessingReferences) Cleanup TGI calls accessing References --- OPEN

1. New Business
2. Future Meetings
   * Jan. 6 & 20, 10:00-11:30 AM EST
   * Feb. 3 & 17, 10:00-11:30 AM EST
3. Second Roll Call
4. Adjourn