IEEE SCC20 16-1 Meeting North Reading, Massachusetts

Committee (e.g.; Steering) Meeting – Conference Center 4:00PM – 5:00PM Wednesday May 25, 2016 Draft 4 Agenda

<u>Items</u> <u>Category</u> <u>Person</u>

1 Call to Order Action Mike Seavey

Mike Seavey called meeting to order at 3:38 pm

2 **Quorum Check** Action Teresa Lopes

Working Group Ch	airs and Designated Representatives	
SCC20 Chair	Mike Seavey	
SCC20 Vice-Chair	Chris Gorringe	
SCC20 Secretary/Treasurer	Teresa Lopes	
P1445 Working Group Chair	Mike Seavey	
P1505.1 Working Group Chair	Rob Spinner	
P1514 Working Group Chair	Steve Mann	
P1636 Working Group Chair	John Sheppard	
P1636.2 Working Group Chair	John Sheppard	
P1641a Working Group Chair	Ion Neag	
P1671.1 Working Group Chair	Anand Jain	
P1671.3 Working Group Chair	Ion Neag	
P1871.1 Working Group Chair	Chris Gorringe	
P1871.2 Working Group Chair	Chris Gorringe	
AES Society Designated Representative	Joe Stanco	
Computer Society Designated Representative	John Sheppard	
I&M Society Designated Representative	Joe Stanco	
DoD Designated Representative	Bill Ross	
MoD Designated Representative	Malcolm Brown	
NDIA Designated Representative	Les Orlidge	

Committee Members: Mike Seavey, Chris Gorringe, Teresa Lopes, John Sheppard, Joe Stanco, Bill Ross, Malcolm Brown, Les Orlidge, Rob Spinner, Steve Mann, Ion Neag, Anand Jain

There are 10 members in attendance, which satisfies the requirement for a quorum of 6 (quorum is 50% and there are currently 12 members)

2.1	Introductions/Attendance Sheets	Information	Mike Seavey

3 Agenda

3.1 Accept Agenda Action Mike Seavey

Review of agenda. Two new items to discuss: new PARs, new study group

4 Old Business

4.1 Review/Accept Minutes from Previous Meeting

Action

Teresa Lopes

Meeting from previous meeting accepted as posted.

Note: Secretary is responsible only for committee and steering minutes. Working group minutes are the responsibility of the working group. For this meeting, send minutes to Teresa (the website is under construction).

- 6 Updated WG and Study Group Reports
- 6.1 P1445 Information Mike Seavey

Document reviewed. Going forward to ballot after this meeting.

6.2 P1505.1 Information Rob Spinner

Reviewed edited document. Identified additions and subtractions and got the document in sync with the rest of the standards. Will continue to review the document in a conference call and get it out to ballot as soon as possible.

6.3 P1514 Information Steve Mann

Looking at adding some ATML intrinsic path information to the description. Breaking down the document into smaller chunks to help with the implementation.

6.4 P1636 & P1636.2 Information John Sheppard

Discussed the progress on both documents. Initial goal is to have everything in place by September, allowing for one year for the ballot. The working group would like both documents to ballot together. Removing EXPRESS from base document. Rolling .99 into base document and creating an OWL ontology. A lot of work, but looking not bad.

- 6.5 P1641.1a Information Ion Neag
 Reviewed standard serial bus TSFs. These will give us a good set of examples that people can download and reference. Also looked at
- parallel digital including using DTIF, these will also be provided as examples. Hoping to have a draft by the end of the year.
 - 6.6 P1671.1 Information Anand Jain

Discussed support for serial and parallel digital signals. Most of the remaining work is updating the schema which will be done offline. Updated schemas will be published to box net by the end of June. Notify Ion and Anand if you need access to box net.

6.7 P1671.3 Information Ion Neag

Being revised in conjunction with .1 to remove overlap. When looking at models for serial buses, some of that information goes in UUT Description, not in Test Description. Will publish them together with synchronized examples.

6.8 P1871.2 Information Chris Gorringe

Reviewed comments on best practices guide and schema. There was some overlap with Test Adapter that was addressed.

6.9 1505 Study Group Information Steve Mann

Collecting input from the user community. Developing a list of users for this interface. Want to reach out to user community (programs and systems where it is used) and see if they have any recommendations and input on what should be changed.

6.10 1232 Study Group Information John Sheppard

Called to figure out what to do about AIESTATE. Published in 2010 due to expire in 2020. Decision was made to let it go inactive. Form a study group to create a new project with the same basic approach but with a new look. Emphasis on simplicity (standard is easier to understand). Correct perception that it is difficult. Move from EXPRESS to OWL. Find a champion for the standard to increase its chances of success. Include PHM.

- 7 Liaison Reports
- 7.1 Aerospace Electronic Systems Society Information Joe Stanco Nothing to report
- 7.2 Instrumentation and Measurement Society Created an update for the society describing what SCC20 does.
- 7.3 Computer Society Information John Sheppard Not a lot going on that directly relates to what is going on in this group.
- 7.4 National Defense Industrial Association Information Les Orlidge See NDIA ATC Liaison Report for SCC20 16-1

7.5 U.S. DoD Information Bill Ross

DoD ATS Management board met last week. Reviewed each services plans for ATS. Not a lot of changes. Budget challenges. Each of the IPT reported status. Standards IPT progress with standards was noted. FPGAs and CyberSecurity were hot topics. CyberSecurity IPT formed. FPGA Working group formed under the NxTest IPT (led by John Stabler). Some standards work will probably come out of that working group.

CyberSecurity and smart diagnostics dilemma – some of the services have shut down the work to do diagnostics because of the CyberSecurity requirements.

Open Architecture strategy for Naval Aviation – shows renewed emphasis is this area.

Smart Diagnostics is taken the hit because of Cyber Security threat – afraid of malicious data. Threat to UUTs. Inconsistencies about how CyberSecurity is handled across the different groups.

eCASS analysis shows how a UUT accidentally infected at the factory could with 3 iterations infect 90% of the fleet.

7.6 U.K. MoD No report

8 New Business

8.1 Draft Standards to Sponsor Ballot Action Mike Seavey
1445 – Revision to 1998 standard that was reaffirmed. Includes the users manual – combined into one document. Three actions that need to be addressed. Once those are addressed (all editorial) the document is ready to go to ballot.

John Sheppard moves that we proceed with 1445 Sponsor ballot. Les seconds. Motion passes.

1871.2 – Minor edits needed to the ballot. Schemas and document have been out for review. Minor changes to the schema were made at this meeting. The changes to the schema need to be rolled into the standard.

Chris Gorringe motions that the sponsor ballot for 1871.2 get initiated by Mike. Les Orlidge seconds. Ion Neag asks "Is it ready?" Document and schema have been out for review. It is ready. Motion passes.

8.2 SCC20 16-2 & 17-1 Meeting Information Mike Seavey See SCC20 16-1 Upcoming Meetings

9 **Adjournment** Action Mike Seavey Meeting adjourned at 4:40 PM

NDIA ATC Liaison report for SCC20 16-1 Steering Committee and Minutes

NDIA Liaison Officer: Les Orlidge

Report:

The Automatic Test Committee (ATC) of the National Defense Industrial Association (NDIA) held its Spring 2016 meeting 26 April at NDIA HQ in Arlington, Virginia.

April meeting highlights included:

- 1. Liaison reports covering status of and news related to the following automatic test related organizations:
 - DoD/ATS Mgmt. Board (AMB) Bill Ross
 - US Army (TMDE) Les Orlidge (for Scott Brown)
 - US Marine Corps (Ground) Lou Salzano
 US Navy/Marine Corps (Air) Tony Minei
 US Air Force Frank Bruno
 - Commercial Test Industry Les Orlidge (for Mark Holtzer)
- 2. This was followed by discussions regarding potential projects:
 - a. "FPGA" discussions centered around identifying a project to address issues associated with FPGA test and support, e.g.
 - i. Multiple manufacturers, each with their own sets of development tools.
 - ii. Programming data not consistent between manufacturers for the same functionality.
 - iii. Where to put the emphasis in FPGA test? TPS or ATE or somewhere in between? How do we control the interface boundaries between vendor, ATE, and TPS programming?
 - iv. No DoD standards or programming style preferences. This can lead to portability problems and obsolescence issues.
 - b. "Standardized Software Architecture for Synthetic Instrumentation" The chair asked DoD attendees to discuss the need for this project at the next AMB and NxTest IPT meetings.

The next full committee meeting will be held in conjunction with AUTOTESTCON at 1pm on Sunday, 11 September in Anaheim California.

The meeting agenda and registration information will be posted at: http://www.ndia.org/meetings/687G/Pages/default.aspx

Upcoming SCC20 Meetings



Planned Upcoming SCC20 Meetings

- SCC20 16-2
 - Location: The Disneyland Hotel, Anaheim, CA
 - When: September 10-11
 - Who: Active SCC20 working groups all day Saturday and Sunday afternoon
 - SCC20 "Steering" on Sunday, 10:00-Noon

Note: 2016 deadline for submittals is October 17, 2016

IEEE Autotestcon is September 12-15



Planned Upcoming SCC20 Meetings

- SCC20 17-1
 - Location: TBD
 - When: The dates are TBD Spring 2017
 - Who: Active SCC20 working groups
 - SCC20 "Steering"

